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Debugging and Profiling with DDT and Map

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Agenda

- General Debugging and Profiling Advice
- Arm Software for Debugging and Profiling
- Debugging with DDT
- Profiling with MAP
- Theta Specific Settings



Debugging

Transforming a broken program to a working one How? TRAFFIC!

- -Track the problem
- -Reproduce
- -Automate (and simplify) the test case
- -Find origins where could the "infection" be from?
- -Focus examine the origins
- -Isolate narrow down the origins

-Correct – fix and verify the test case is successful



Profiling

Profiling is central to understanding and improving application performance.



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Performance Improvement Workflow





Arm Software



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Arm Forge

An interoperable toolkit for debugging and profiling

The de-facto standard for HPC development

- Available on the vast majority of the Top500 machines in the world
- Fully supported by Arm on x86, IBM Power, Nvidia GPUs, etc.

State-of-the art debugging and profiling capabilities

- Powerful and in-depth error detection mechanisms (including memory debugging)
- Sampling-based profiler to identify and understand bottlenecks
- Available at any scale (from serial to parallel applications running at petascale)

Easy to use by everyone

- Unique capabilities to simplify remote interactive sessions
- Innovative approach to present quintessential information to users





Arm Performance Reports

Characterize and understand the performance of HPC application runs



Commercially supported by Arm



Accurate and astute insight



Relevant advice to avoid pitfalls

Gathers a rich set of data

- Analyses metrics around CPU, memory, IO, hardware counters, etc.
- Possibility for users to add their own metrics

Build a culture of application performance & efficiency awareness

- Analyses data and reports the information that matters to users
- Provides simple guidance to help improve workloads' efficiency

Adds value to typical users' workflows

- Define application behaviour and performance expectations
- Integrate outputs to various systems for validation (e.g. continuous integration)
- Can be automated completely (no user intervention)



Run and ensure application correctness

Combination of debugging and re-compilation

- Ensure application correctness with
- Integrate with continuous integration system.
- Use version control to track changes and leverage Forge's built-in VCS support.

Examples:

\$> ddt --offline aprun -n 48 ./example
\$> ddt --connect aprun -n 48 ./example



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	9	A	Araon	25 main (cpi.c:46) 0-3 done: — 0 i: ⁻ from 97 to 100 numprocs: — 8	s myid: from 0 to 3 n: 100

Understand application behaviour

Set a reference for future work

- Choose a representative test case with known behavior
- Analyse performance with Arm Performance Reports

Example:

\$> perf-report aprun -n 16 mmult_c.exe

Is it performant?



Summary: mmult_c.exe is Compute-bound in this configuration



This application run was Compute-bound. A breakdown of this time and advice for investigating further is in the CPU section below.

As little time is spent in MPI calls, this code may also benefit from running at larger scales.

CPU

A breakdown of the 62.8% CPU time: Scalar numeric ops 0.2% | Vector numeric ops 13.4% Memory accesses 80.3%

The per-core performance is memory-bound. Use a profiler to identify time-consuming loops and check their cache performance.

MPI

A breakdown of the 24.6% MPI time:

 Time in collective calls
 6.3%

 Time in point-to-point calls
 93.7%

Effective process collective rate 0.00 bytes/s | Effective process point-to-point rate 114 MB/s

Most of the time is spent in point-to-point calls with an average transfer rate. Using larger messages and overlapping communication and computation may increase the effective transfer rate.

Memory

Per-process memory usage may also affect scaling: Mean process memory usage 448 MiB

Peak process memory usage 1.24 GiB

Peak node memory usage 16.0%

There is significant variation between peak and mean memory usage. This may be a sign of workload imbalance or a memory leak.

The peak node memory usage is very low. Running with fewer MPI processes and more data on each process may be more efficient.

1/0

A breakdown of the 12.6	% I/O time:	
Time in reads	0.0%	
Time in writes	100.0%	ľ
Effective process read rate	0.00 bytes/s	J.
Effective process write rate	3.56 MB/s	

Most of the time is spent in write operations with a very low effective transfer rate. This may be caused by contention for the filesystem or inefficient access patterns. Use an I/O profiler to investigate which write calls are affected.

Threads

A breakdown of how multiple threads were used:

Computation	0.0%	
Synchronization	0.0%	1
Physical core utilization	99.7%	-

System load 101.8%

No measurable time is spent in multithreaded code.



Optimize the application for Arm

- Measure all performance aspects with
- Identify bottlenecks and rewrite some code for better performance

Examples:

```
$> map --profile aprun -n 48 ./example
```

Profiled: clover_leaf on 32 proce	esses, 4 nodes, <u>32 cores (1 per proc</u>	ess) Sampled from: Wed Nov 9 2016 15:28:37 (UTC) for 309.1s	Hide Metrics
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0 - 5,575.1 (341.0 avg)			
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Total Time ✓ MPI 63.0% 31.4 16.9% 5.3% 12.8% 6.3% 6.3% 6.3%	 vel vort 3d fp., <un< li=""> time_integration mod_rank_read_file velocity_solver velocity_solver </un<>	call the_integration call the_integration call wed_rank_read_file_all_its_own(str,nn,ios) ! Restart from last checkpoint call velocity_solver call velocity_solver	My_code.f90:143 My_code.f90:330 My_code.f90:297 My_code.f90:337
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Debugging with DDT



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Arm DDT – The Debugger

Who had a rogue behaviour ?

– Merges stacks from processes and threads Where did it happen?

- leaps to source How did it happen?
- Diagnostic messages
- Some faults evident instantly from source Why did it happen?
- Unique "Smart Highlighting"
- Sparklines comparing data across processes



Preparing Code for Use with DDT

As with any debugger, code must be compiled with the debug flag typically -g

It is recommended to turn off optimization flags i.e. -00

Leaving optimizations turned on can cause the compiler to optimize out some variables and even functions making it more difficult to debug



Segmentation Fault

In this example, the application crashes with a segmentation error outside of DDT.

🔚 Te	rminal - rhulguin@ryanlinux:/media/sf_VM_share/Training_Codes/1_2_cstartmpi/f90 🛧 🗕 🗆 🗙
File	Edit View Terminal Tabs Help
#2	0x7FC085B8E66F
#O	0x7FEF17094467
#1	0x7FEF17094AAE
#2	0x7FEF1637F66F
#3	0x4017EB in func3 at cstartmpi.f90:103
#4	0x4014B8 in cstartmpi at cstartmpi.f90:62
#0	0x7F585EDF6467
#1	0x7F585EDF6AAE
#2	0x7F585E0E166F
mpir	un noticed that process rank 12 with PID 18305 on node
rem	otemachine exited on signal II (Segmentation fault).
[rbu	lauinenvonlinuv f00lt
Lrnu	nguin@ryantinux 190]\$

What happens when it runs under DDT?



Segmentation Fault in DDT



DDT takes you to the exact line where Segmentation fault occurred, and you can pause and investigate



Invalid Memory Access



The array tab is a 13x13 array, but the application is trying to write a value to tab(4198128,0) which causes the segmentation fault.

 \mathbf{i} is not used, and \mathbf{x} and \mathbf{y} are not initialized



It works... Well, most of the time



A strange behaviour where the application "sometimes" crashes is a typical sign of a memory bug

Arm DDT is able to force the crash to happen



Advanced Memory Debugging

		 Preload the memory debugging library 	Languag
Run		Note: Preloading only works for programs li program is statically linked, you must relink manually.	inked ag it again
tun: mpirun -n 4 examples/wave_c	Details	Heap Debugging	
Command: mpirun -n 4 examples/wave_c		Fast Balanced	
OpenMP			
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Help Options	<u>R</u> un Quit	Ad <u>v</u> anced	🔹 he
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		0 100%	Select A
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Memory Debugging Options ge: C/Fortran, no threads 🗘 ainst shared libraries. If your st the dmalloc library Thorough Custom More Information access \$ ap operations ll x2 x0.5 1% OK Cancel



Heap debugging options available



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Guard pages (aka "Electric Fences")



- A powerful feature...:
 - Forbids read/write on guard pages throughout the whole execution

(because it overrides C Standard Memory Management library)

- ... to be used carefully:
 - Kernel limitation: up to 32k guard pages max ("mprotect fails" error)
 - Beware the additional memory usage cost



Allinea DDT 4-2-1-36484	_ = X
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Create Group	
Project Files 🖉 🗷 🕐 MpiEnvironment.cc 💥 💽 xyzpart.c 💥	Locals Current Line(s) Current Stack
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Computer III titan-ext7 Allinea DDT 4.2.1-36484	Sun Aug 10, 7:50 PM

New Bugs from Latest Changes





Track Your Changes in a Logbook

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				Ready

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Inspect AVX Registers

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Arm DDT Demo



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Five great things to try with Allinea DDT













Arm DDT cheat sheet

Load the environment module

- \$ module load forge/18.2.1

Prepare the code

- \$ cc -O0 -g myapp.c -o myapp.exe

Start Arm DDT in interactive mode

- \$ ddt aprun -n 8 ./myapp.exe arg1 arg2

Or use the reverse connect mechanism

- On the login node:
 - \$ ddt &
- (or use the remote client) <- Preferred method</p>
- Then, edit the job script to run the following command and submit:
 - ddt --connect aprun -n 8 ./myapp.exe arg1 arg2



Profiling with MAP



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Arm MAP – The Profiler







Glean Deep Insight from our Source-Level Profiler





Initial profile of CloverLeaf shows surprisingly unequal I/O

Each I/O operation should take about the same time, but it's not the case.





Symptoms and causes of the I/O issues

Sub-optimal file format and surprise buffering.



- Write rate is less than 14MB/s.
- Writing an ASCII output file.
- Writes not being flushed until buffer is full.
 - Some ranks have much less buffered data than others.
 - Ranks with small buffers wait in barrier for other ranks to finish flushing their buffers.
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Solution: use HDF5 to write binary files

Using a library optimized for HPC I/O improves performance and portability.

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Solution: use HDF5 to write binary files

Using a library optimized for HPC I/O improves performance and portability.



- Replace Fortran write statements with HDF5 library calls.
 - Binary format reduces write volume and can improve data precision.
 - Maximum transfer rate now 75.3 MB/s, over 5x faster.
- Note MPI costs (blue) in the I/O region, so room for improvement.



Arm MAP cheat sheet

Load the environment module (manually specify version)

- \$ module load forge/18.2.1

Generate the wrapper libraries (static is default on Theta)

– \$ make-profiler-libraries --lib-type=static

Unload Darshan module (It wraps MPI calls which cannot be used with MAP)

\$ module unload darshan

Follow the instructions displayed to prepare the code

- \$ cc -O3 -g myapp.c -o myapp.exe -WI,@/path/to/profiler_wrapper_libraries/allinea-profiler.ld
- Edit the job script to run Arm MAP in "profile" mode
- \$ map --profile aprun -n 8 ./myapp.exe arg1 arg2

Open the results

- On the login node:
 - \$ map myapp_Xp_Yn_YYY-MM-DD_HH-MM.map
- (or load the corresponding file using the remote client connected to the remote system or locally)



Six Great Things to Try with Allinea MAP





Theta Specific Settings



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Configure the remote client

Install the Arm Remote Client

 Go to : <u>https://developer.arm.com/products/software-development-</u> tools/hpc/downloads/download-arm-forge

Connect to the cluster with the remote client

- Open your Remote Client
- Create a new connection: Remote Launch → Configure → Add
 - Hostname: <username>@theta.alcf.anl.gov
 - Remote installation directory:

/soft/debuggers/forge-18.2.1-2018-08-07

- ALCF Documentation available at

https://tinyurl.com/debugging-cpw-2018-05



Static Linking Extra Steps

To enable advanced memory debugging features, you must link explicitly against our memory libraries

Simply add the link flags to your Makefile, or however appropriate

Iflags = -L/soft/debuggers/ddt/lib/64 -WI,--undefined=malloc -Idmalloc -WI,--allowmultiple-definition

In order to profile, static profiler libraries must be created with the command make-profiler-libraries --lib-type=static

Instructions to link the libraries will be provided after running the above command



Questions?



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Thank You! Danke! Merci! 谢谢! ありがとう! **Gracias!** Kiitos! 감사합니다 धन्यवाद

